

EAST - [896071b:1] File View Edit Tools Window Help

Active

- L1: (1067) evaluat\$4 same cell same design
- L2: (6002) cell near9 specification
- L3: (52) 11 and 12
- L4: (6) 13 and hdl and netlist
- L5: (4) 14 and context
- L6: (3) 15 and library
- L7: (2) 15 and library and map\$4
- L8: (2) 15 and map\$4
- L9: (1531) ("transistor-level" or (transistor adj3 level))same cell
- L10: (2) 14 and 19
- L11: (21832) cell near9 design
- L12: (9293) map\$4 near9 cell
- L13: (16) 112 and 111 and 19
- L14: (494) library and hdl and netlist
- L15: (3) 113 and 114

Failed

Searched

Search List Browse Queue
DBs UK Plurals
Default operator Highlight all hit terms initially

113 and 114

	U	I	Document ID	Issue Date	Pages	Title	Current OR
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020073380 A1	20020613	89	Block based design methodology with programmable components	716/1
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020069396 A1	20020606	20	Method for automated design of integrated circuits with targeted quality objectives	716/7
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6083271 A	20000704	15	Method and apparatus for specifying multiple power domains in electronic circuit designs	716/1

Details HTML

Ready

EAST - [896071a:1]

File View Edit Tools Window Help

Drafts Pending Active

- L1: (39932) design\$3 near9 cell
- L2: (1160) design same cell same specification
- L3: (146) map\$4 same cell same transistor
- L4: (253) evaluat\$4 same cell same specification
- L5: (2) 11 and 12 and 13 and 14

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11 and 12 and 13 and 14

BRS f... IS&R ... Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020069396	20020606	20	Method for automated design of integrated circuits with targeted quality objectives	716/7
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20020053063	20020502	13	Process for automated generation of design-specific complex functional blocks to	716/1

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